Please amend claim 2 as follows:

2.(Amended) A[The] sound processing system [of claim 1]comprising:

a speaker;

an integrated circuit having a first terminal coupled to the speaker, the integrated circuit further comprising:

an output circuit coupled to the first terminal, wherein the output circuit applies to the first terminal an output signal to drive the speaker;

an input circuit coupled to the first terminal, wherein the input circuit processes an input signal from the speaker via the first terminal; and

[, wherein the integrated circuit further comprises] a multiplexing circuit coupled between the first terminal and the output and input circuits.

A1

Please amend claim 4 as follows:

4.(Amended) A[The] sound processing system [of claim 3]comprising:

a speaker;

an integrated circuit having a first terminal coupled to the speaker, the integrated circuit further comprising:

Sub D1

an output circuit coupled to the first terminal, wherein the output circuit applies to the first terminal an output signal to drive the speaker;

A2

an input circuit coupled to the first terminal, wherein the input circuit processes an input signal from the speaker via the first terminal;

a functional unit; and

an activation circuit that activates the functional unit in response to the input signal from the speaker exceeding a threshold level, wherein the functional unit is coupled to the output circuit and begins an output operation to drive the speaker in response to being activated by the activation circuit.

Please amend claim 6 as follows:

6.(Amended) The system of claim 5, wherein the input circuit comprises:

A3

an amplifier coupled to the [input/output pin]first terminal;

a second converter coupled to the amplifier and the access circuitry, wherein the second converter converts the input signal from the speaker into a series of values read that the access circuitry writes into the memory array.

Please amend claim 7 as follows:

7.(Amended) The system of claim [3]4, wherein the activation circuit includes a delay element coupled to prevent the activation of the functional unit during a period following the completion of an operation of the functional unit.

Please amend claim 8 as follows:

8.(Amended) A[The] sound processing system [of claim 1, further]comprising:

a speaker;

an integrated circuit having a first terminal coupled to the speaker, the integrated circuit further comprising:

an output circuit coupled to the first terminal, wherein the output circuit applies to the first terminal an output signal to drive the speaker;

an input circuit coupled to the first terminal, wherein the input circuit processes an input signal from the speaker via the first terminal;

a memory array; and

access circuitry capable of reading values from the memory array, wherein:

the output circuit comprises a converter coupled to access circuitry, wherein the converter converts a series of values read by the access circuitry into an analog signal that determines the output signal.

Please amend claim 9 as follows:

9.(Amended) The system of claim 8, wherein the input circuit comprises:

an amplifier coupled to the [input/output pin]first terminal;

a second converter coupled to the amplifier and the access circuitry, wherein the second converter converts the input signal from the speaker into a series of values read that the access circuitry writes into the memory array.

Please amend claim 11 as follows:

11.(Amended) A[The] sound processing system [of claim 1,] comprising:

a speaker;

an integrated circuit having a first terminal coupled to the speaker, the integrated circuit further comprising:

an output circuit coupled to the first terminal, wherein the output circuit applies to the first terminal an output signal to drive the speaker; and

an input circuit coupled to the first terminal, wherein the input circuit processes an input signal from the speaker via the first terminal,

wherein the integrated circuit is in a three in package including a first pin connected to the speaker and the first terminal of the integrated circuit, a second pin for connection to a power supply, and a third pin for connection to ground.

Please amend claim 17 as follows:

17.(Amended) The [integrate] integrated circuit of claim 16, wherein the sound processing circuit comprises:

a memory array;

a read circuit coupled to the memory array, wherein the read circuit is part of the first functional unit and the output operation includes reading from the memory array a series of values representing a sound; and

a write circuit coupled to the memory array, wherein the write circuit is part of the second functional unit and the input operation includes writing to the memory array a series of values representing the input signal.

Please amend claim 18 as follows:

18.(Amended) The integrated circuit of claim 13, wherein the activation circuit comprises a delay element coupled to prevent the activation circuit from activating the sound processing circuit during a period following the completion of an operation by the sound processing circuit.

Please add the following new claims:

--25. The system of claim 6, wherein the output signal is derived from said series of values.

26. The system of claim 9, wherein the output signal is derived from said series of values.

27. The integrated circuit of claim 17, wherein the output signal is derived from said series of values.

28. The method of claim 20, further comprising:

recording an audio input by said functional unit through the speaker prior to creating the vibration, wherein the output signal is derived from the audio input.--

REMARKS

These remarks are in response to the Office Action mailed on January 19, 2000, and for which a one month extension is hereby requested is hereby requested. In that Office Action, all of the pending claims, claims 1-24, were rejected. Claim 18 was additionally objected to and has been amended to conform with the remarks of the Office Action. Claims 6 and 9 were rejected under 35 U.S.C. 112 and have been amended appropriately. Claims 1-6, 8-10, 13-17 and 20-21 were rejected under 35 U.S.C. 102(b) as being anticipated by Bobry, U.S. Patent No. 5,593,236, with the remaining claims rejected under 35 U.S.C. 103(a) over Bobry in view one of several additional references as noted below. Claims 1, 3, and 10 have been cancelled. Accordingly, claim 7 has had its dependence changed and claims 2, 4, 8, and 11 have been amended to incorporate their respective underlying, and